

Fig.1 (Prior Art) Alpha Format Packet

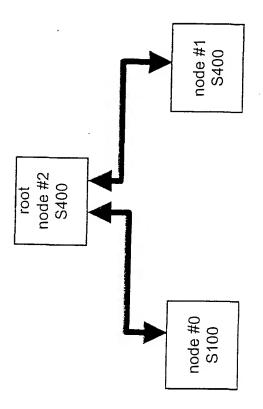


Fig. 2 (Prior Art) Legacy Compatibility Example

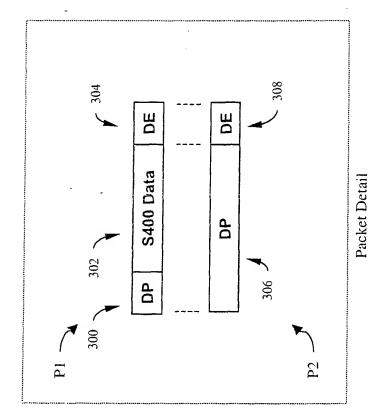
Express Mail: EV263396186US Inventor: Hauck et al.

Docket No.: APPL-P2834 DVB Page 3 of 13

Filed: August 5, 2003

Title: METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS

Attorney: John W. Crosby Sierra Patent Group, Ltd.



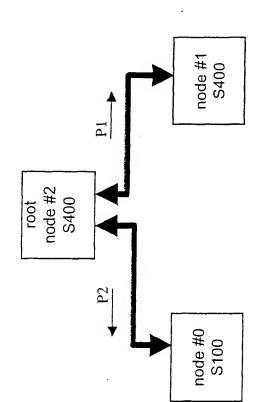


Fig. 3 (Prior Art) Legacy Speed Filtering Example

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 $\left\{ \frac{\text{Sb1}}{\text{2b1}} \text{ DP1} \text{ B1} \dots \text{Bn} \text{ DE1} \text{ DE2} \right\}$

Fig. 4 (Prior Art) P1394b Packet Format

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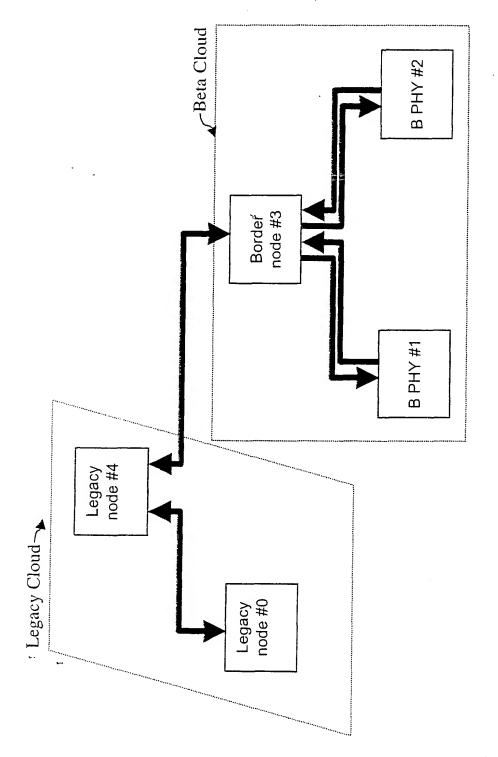


Fig. 5 (Prior Art)

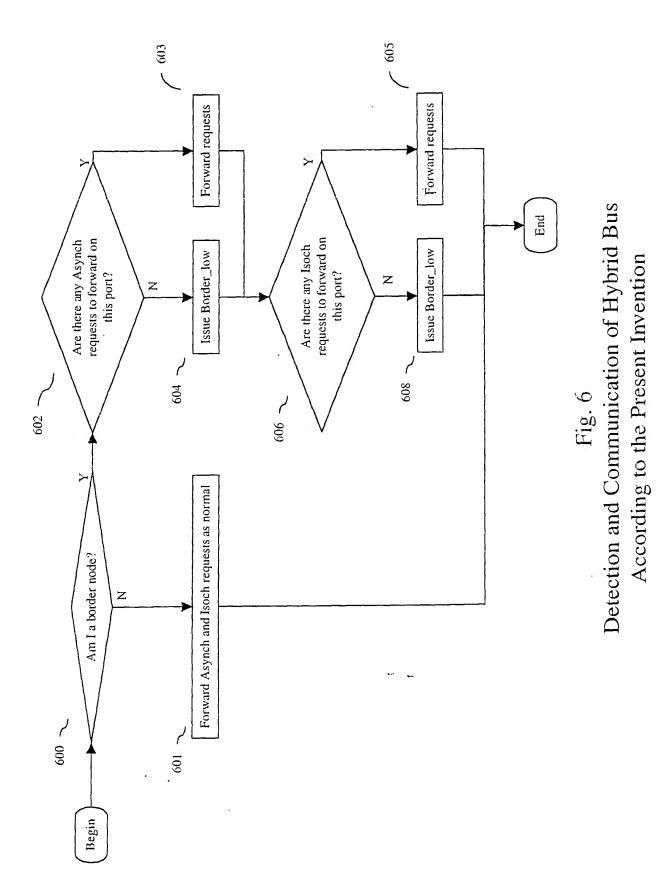
Data Communications System Having both
P1394b and Legacy Devices

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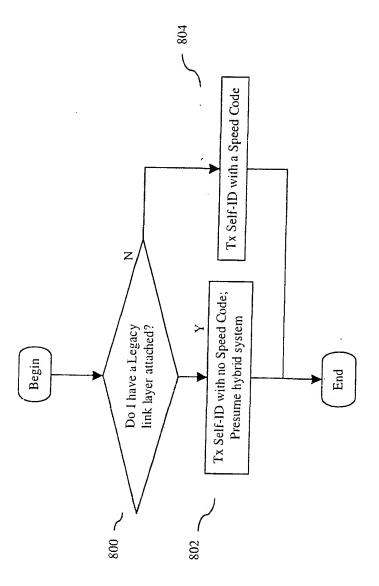
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DP1 Speed Code DP2 Data ...

Fig. 7 (Prior Art) Legacy Format Packet



Detection and Communication of a Legacy Link Layer According to the Present Invention Fig. 8

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900

Rx Self-ID packet with
no Speed Code?

Presume hybrid system

End

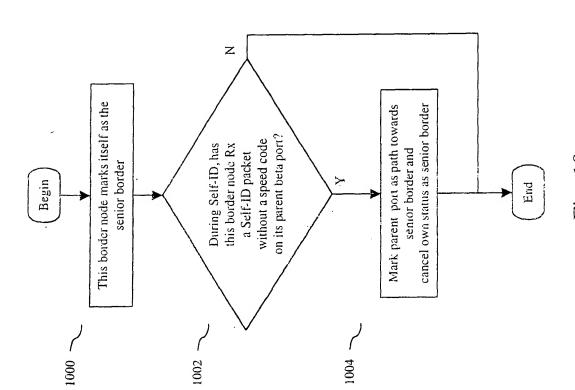
Fig. 9
Detection and Communication of Hybrid Bus
According to the Present Invention

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According to the Present Invention Detection of Path to Senior Border By a Border PHY Fig. 10

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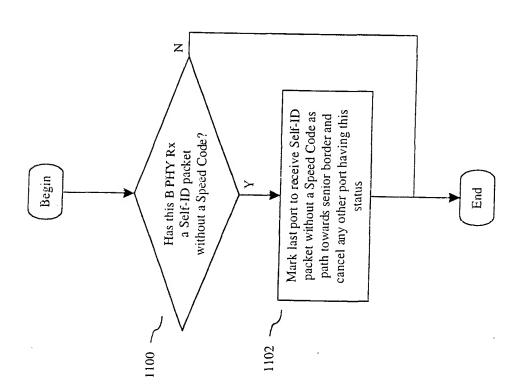
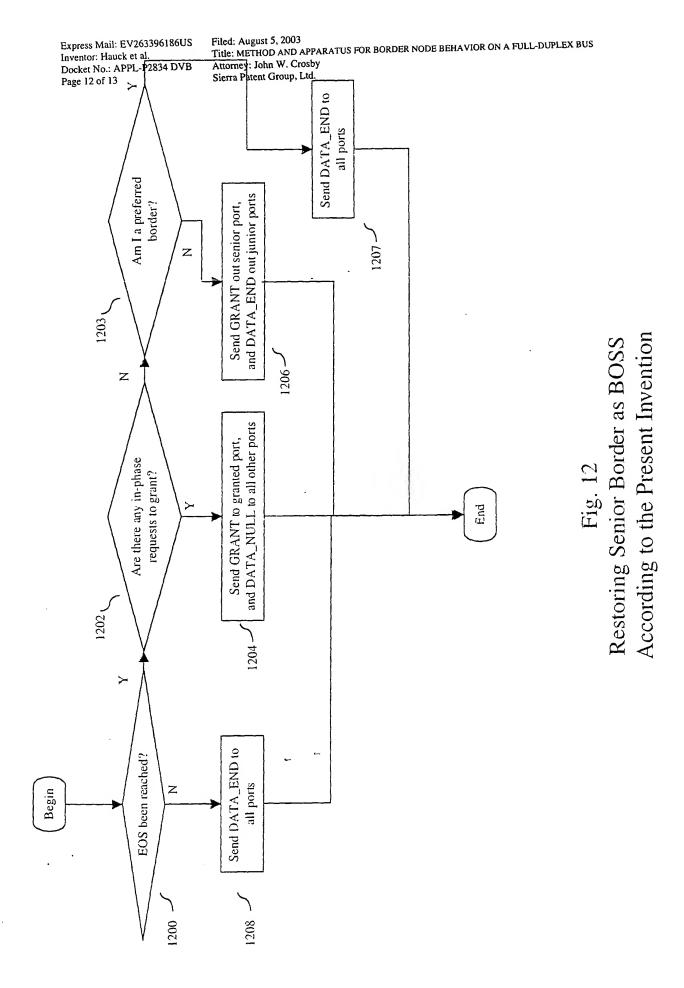


Fig. 11
Identification of Senior Border Node
By a B PHY
According to the Present Invention



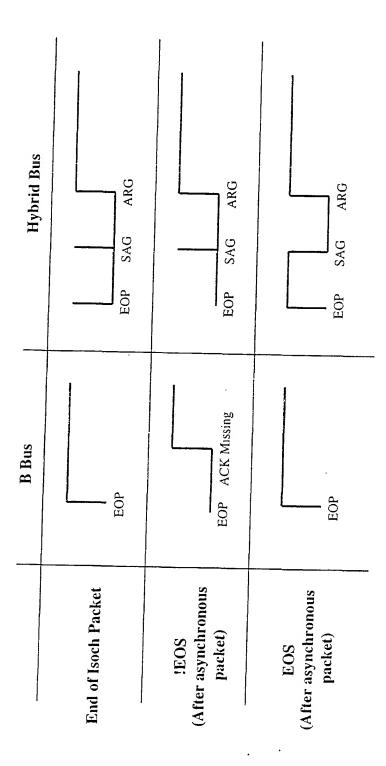


Fig. 13
OK_TO_GRANT Profile
According to the Present Invention